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STATEMENT BY APPLICANT**

(Not for submission under 37 CFR 1.99)

Application Number	10511504
Filing Date	2007-03-15
First Named Inventor	BERNARDO DE OLIVEIRA KASTRUP PEREIRA
Art Unit	2181
Examiner Name	WILLIAM M. TREAT
Attorney Docket Number	NL020320

U.S. PATENTS

Examiner Initial*	Cite No	Patent Number	Kind Code ¹	Issue Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear
WMT	1	504 ⁶ 000		1991-09-03	HSU	
WMT	2	5295252		1994-03-15	TORII	

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WMT	1	EP 0729106	EP	A1	1996-08-26	FRAMPTON		<input type="checkbox"/>
WMT	2	EP 0717347	EP	A2	1996-06-19	SMOLANSKY		<input type="checkbox"/>

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WMT	1	EMILIO L. ZAPATA, "A VLSI Constant Geometry Architecture for the Fast Hartley and Fourier Transforms", 8354 IEEE Transactions on Parallel and Distributed Systems, January 1992, no. 1, NY	<input type="checkbox"/>
WMT	2	J. KANG VAN DER WERF et al., "Mapping Array Communication onto FIFO Communication - Towards an Implementation", Proceedings 13th Int'l Symposium on Systems Synthesis, pp. 207-213, Published: Los Angeles, CA	<input type="checkbox"/>
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Examiner Signature	W. TREAT	Date Considered	3/23/07
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